

Appl. No.: 09/839,624
Amdt. dated June 3, 2004
Reply to Office action of March 9, 2004

Amendments to the Specification:

Please replace paragraph [0001] with the following amended paragraph:

[0001] U.S. Patent Application No. 09/584,034, filed May 30, 2000, and entitled "Slack Fetch to Improve Performance in of a Simultaneous and Redundantly Threaded Processor[.]" ~~Attorney Docket No. 1662-23801;~~

Please replace paragraph [0002] with the following amended paragraph:

[0002] U.S. Patent Application No. 09/837,995, filed April 19, 2001, and entitled "Simultaneous and Redundantly Threaded Processor Store Instruction Comparator[.]" ~~Attorney Docket No. 1662-36900;~~

Please replace paragraph [0003] with the following amended paragraph:

[0003] U.S. Patent Application No. 09/839,459, filed April 19, 2001, and entitled "Cycle Count Replication in a Simultaneous and Redundantly Threaded Processor[.]" ~~Attorney Docket No. 1662-37000;~~

Please replace paragraph [0004] with the following amended paragraph:

[0004] U.S. Patent Application No. 09/839,621, filed April 19, 2001, and entitled "Active Load Address Buffer[.]" ~~Attorney Docket No. 1662-37100;~~

Please replace paragraph [0005] with the following amended paragraph:

[0005] U.S. Patent Application No. 09/838,078, filed April 19, 2001, and entitled "Simultaneous and Redundantly Threaded Processor Branch Outcome Queue[.]" ~~Attorney Docket No. 1662-37200;~~

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Please replace paragraph [0006] with the following amended paragraph:

[0006] U.S. Patent Application No. 09/838,069, filed April 19, 2001, and entitled "Input Replicator for Interrupts in a Simultaneous and Redundantly Threaded Processor[.]" ~~Attorney Docket No. 1662-37300;~~

Please replace paragraph [0007] with the following amended paragraph:

[0007] U.S. Patent Application No. 09/839,626, filed April 19, 2001, and entitled "Simultaneous and Redundantly Threaded Processor Uncached Load Address Comparator and Data Value Replication Circuit[.]" ~~Attorney Docket No. 1662-37400.~~